

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Express Mail No. EV 352375545 US

Applicant : Janardhanan S. Ajit  
Application No. : To be Assigned  
Filed : Filed Herewith  
Title : SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT  
OUTPUT (I/O) CIRCUIT  
Grp./Div. : To be Assigned  
Examiner : To be Assigned  
Docket No. : 50717/PAN/B600

INFORMATION DISCLOSURE STATEMENT  
FOR A CONTINUING NON-CPA APPLICATION

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

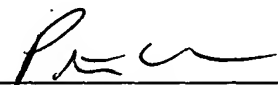
PostOffice Box 7068  
Pasadena, CA 91109-7068  
July 16, 2003

Commissioner:

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, enclosed is FORM PTO/SB/08A/B. The references listed thereon are not enclosed because they were of record in a parent application, Application No. 10/043,788, filed January 9, 2002.

It is respectfully requested that these references be considered in the examination of this application, and identified in the list of references cited on the patent issuing on this application.

Respectfully submitted,  
CHRISTIE, PARKER & HALE, LLP

By   
Peter A. Nichols  
Reg. No. 47,822  
626/795-9900

PAN/cks  
Enclosure(s)  
CKS PAS515263.1.\*-07/16/03 9:23 AM

FORM PTO/SB/08A/B (10-01) Substitute for PTO-1449A/B	<b>Attorney Docket Number</b>	<b>50717/PAN/B600</b>
<b>INFORMATION DISCLOSURE</b>  <b>STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)	Application Number	To be Assigned
	Filing Date	Filed Herewith
	Applicant(s)	Janardhanan S. Ajit
	Group Art Unit	To be Assigned
	Examiner Name	To be Assigned

U.S. PATENT DOCUMENTS				
EXAMINER INITIALS	Cite No. <sup>1</sup>	DOCUMENT NUMBER Number - Kind Code <sup>2</sup> (If Known)	Publication Date MM-DD-YYYY	Name of Patentee
	1	4,473,758	09-25-1984	Huntington
	2	4,556,804	12-03-1985	Dewitt
	3	5,170,078	12-08-1992	Hsueh et al.
	4	5,184,031	02-02-1993	Hayakawa et al.
	5	5,208,488	05-04-1993	Takiba et al.
	6	5,350,951	09-27-1994	Adachi
	7	5,506,535	04-09-1996	Ratner
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	11	5,909,142	06-01-1999	Kawasaki et al.
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	15	6,081,132	06-2000	Isbara, Melik
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	22	6,194,944 B1	02-27-2001	Wert
	23	6,429,716	08-2002	Drapkin et al.

FOREIGN PATENT DOCUMENTS			
EXAMINER SIGNATURE		DATE CONSIDERED	
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at www.pto.gov or MPEP 901.4. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English Language Translation is attached.			

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EXAMINER INITIALS	Cite No. <sup>1</sup>	<u>FOREIGN PATENT DOCUMENT</u>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T <sup>6</sup> (✓)
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (If Known)			
	24	WO 00/38322	06-29-2000	Van Schaik et al.	

OTHER DOCUMENTS		
EXAMINER INITIALS	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	25	DENG-YUAN CHEN, "Design of A Mixed 3.3V and 5V PCI I/O Buffer," <i>Compass Design Automation</i> , San Jose, California, U.S.A.

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